

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Serial No. .... 10/632,273  
Filing Date ..... July 31, 2003  
Inventor ..... Warren M. Farnworth et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2829  
Examiner ..... R.M. Kober  
Attorney's Docket No. .... MI22-2379  
Title: Method and Apparatus for Testing Semiconductor Circuitry for Operability and  
Method of Forming Apparatus for Testing Semiconductor Circuitry for Operability

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References -- See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patent listed on the attached Form PTO-1449. No admission is made regarding whether the submitted reference is prior art.

Citation of this reference is respectfully requested.

Respectfully submitted,

Date: 12-7-04

  
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Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE <b>LIST OF ART CITED BY APPLICANT</b> (Use several sheets if necessary)				ATTY. DOCKET NO. M122-2379	SERIAL NO. 10632,273		
				APPLICANT Warren M. Farngorth et al.			
				FILING DATE July 31, 2003	GROUP 2839		
<b>U.S. PATENT DOCUMENTS</b>							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
AA		6,426,628 B1	07/30/02	Di Stefano			
AB							
AC							
AD							
AE							
AF							
AG							
AH							
AI							
AJ							
AK							
AL							
<b>FOREIGN PATENT DOCUMENTS</b>							
		Document Number	Date	Country	Class	Subclass	Translation
AM							Yes
AN							No
AO							
AP							
AQ							
<b>OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)</b>							
AR							
AS							
AT							
EXAMINER				DATE CONSIDERED			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							